# High Performance in a Wired-and-Wireless Integrated IC Card Ticket System

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**Abstract.** Automatic Fare Collection Systems (AFCSs) require high performance and high reliability. The automatic fare collection gates (AFCGs) must let the passengers pass as quickly as possible because the congested passengers rush to them during rush hours. High performance on wireless communications lowers reliability, which had been a problem when a company was developing a wireless IC card ticket system. "Autonomous Decentralized Processing Technology" and "Decentralized Algorithm on the Fare Calculations with an IC Card and AFCGs" were designed as the solutions. This paper introduces the two models – the decentralized processes for the fare calculations and the workstation-centralized one – to simulate the performances and shows the satisfactory results on the decentralized one. These technologies are implemented to the practical system and have proven the effectiveness.

# **1** Introduction

Declining birthrates, aging, and globalization – The society is changing in Japan and the railway companies are not the exception; they are required to provide more safety, more comfort, and more convenience in addition to the conventional duties such as safety and accuracy. So is the Automatic Fare Collection System (AFCS), one of the most important systems to support the transportation. The conventional AFCS was difficult to coexist with different systems, impossible to satisfy various needs, and hard to be upgraded. The conventional Automatic Fare Collection Gates (AFCGs), for example, forced the passengers to insert their magnetic tickets into the slots and then, to take them away. In addition, the system was expensive to maintain and poor to expand because it was too simple and centralized within the stations. The new AFCS had been expected to solve these problems, to improve the passengers' convenience, and to reduce the maintenance costs.

A wireless IC card ticket system had been taken into consideration in such a mood. In this system, the passengers would simply touch their IC cards to the AFCGs and then go through. The less mechanical parts could decrease the maintenance costs, too. The benefits persuaded the board and the new system was scheduled on the renewal of the conventional magnetic AFCS.

The development of the wireless IC card ticket system is concretely aimed at the gain in the processing speed because it is very important to let the passengers pass through

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In Proceedings of the 5th International Workshop on Wireless Information Systems, pages 56-65 Copyright © SciTePress the AFCGs as smoothly as possible, especially during the rush hours. The walking speed of the passengers is reduced to 1.0m/s along the AFCGs [1] while the random walk in a station shows 1.4m/s [2]. The AFCGs must work as quickly as the people move at this velocity [3][4].

On the other hand, reliability is indispensable to the AFCS because the tickets are worth notes; that is, the AFCS must be equipped with both high performance and high reliability. However, it is difficult for the wireless IC card ticket system because the wireless communications between an IC card and an AFCG are unstable in the manual operations by the passengers.

The unstableness in the manual operations is fixed by introducing the "Touch & Go" method, the way of the operation. The compatibility of high performance and reliability is entrusted to the Autonomous Decentralized Architecture where "IC cards," "AFCGs," and a "center server" are designed as the autonomous units.

This paper focuses on the high performance in the wireless IC card ticket system based on the Autonomous Decentralized Processing Technology. "Autonomous Decentralized Algorithm on the Fare Calculation with an IC Card and AFCGs" is also proposed to achieve the high performance. Two models, with and without this algorithm, are compared in the efficiency.

On the application to the real environment, other technologies for high reliability and assurance [5] are also equipped, which are to be mentioned in the later papers.

# 2 Autonomous Decentralized IC Card Ticket System

The system consists of "IC cards," "terminals" (e.g. AFCGs), "stations servers," and a "center server" and between the nodes exist three different data fields (DFs).

The terminals use wireless communications with the IC cards. The IC cards broadcast the data with an ID code (contents code) to the DF1 and the AFCGs, for example, select the data to collect and to process. The terminals and the station servers are linked to the station LAN and work on the Autonomous Decentralized Processes through the DF2. At the stations the station servers broadcast the data to the DF2 and the terminals such as AFCGs and Ticket Vending Machines (TVMs) select the data to receive. Then the terminals check the "negative" (invalid) cards, calculate the fare, and process any necessary tasks. Each terminal operates autonomously and the failures at some terminals do not influence on the others. When a station server fails, some functions are suspended because the data from the center server do not flow. However, the operations at the station can be kept normal with the stored data in each terminal. The station servers are connected to the center server through the DF3 and thus, the passengers can use all the functions, derived from the DF3 among stations, when they move to another station with a normal station server.

This system is unique with the three different DFs with various time ranges. In the DF1, wireless communications are done within a second while the data flow hourly in the DF2. The data transmission in the DF3 has two cycles: daily and hourly (Fig. 1). These time ranges are varied for the needs and aim at both high performance and high reliability [6][7][8][9].

The minimum operation must be guaranteed in case of failures, which can lead to terrible confusion at stations. This is why the Autonomous Decentralized Architecture

is used. The basic functions of the IC card ticket system are checking the "negative" cards and calculating the fares.



**Fig. 1.** Autonomous decentralized IC card system. Terminals broadcast the fare data every hour and station servers select the necessary data to collect. Station servers broadcast fare data every day and the center server select the necessary data to collect. The center server broadcast "negative" data once a day (every 5 minutes in emergency) and station servers and terminals select the necessary data to collect.

### **3** Autonomous Decentralized Algorithm on Fare Calculations

Fig. 2 shows the fare calculations at the AFCGs. A passenger is supposed to have a commuter pass which allows free rides between the stations X and Y. When he/she got on a train at the station A, out of the free-ride area, he/she had to purchase a conjunction ticket to the closest station J between X and Y. When he/she got off at the station B, out of the area, he/she had to adjust the fare from the closest station K between X and Y. In the IC card ticket system, he/she simply touches the IC card to the AFCGs at the stations A and B. The AFCG at the station A automatically judges that the pass is not valid there, deducting the minimum fare (F0) from the IC card. Then it selects the station J and writes the data such as the station B also judges that the pass is not valid there. Then, it selects the station K and calculates the fare from K to B (FKB). Finally it compares (1) the direct fare from A to B (FAB) minus F0 and (2)

the sum of FAJ and FKB minus F0. The less expensive fare is selected and the IC card is processed so.

It shall take a long time if this complicated calculation is thoroughly processed at the station B. The solution is the "Autonomous Decentralized Algorithm on the Fare Calculations." In this algorithm, the fares are autonomously calculated twice: the "Pre-boarding Process" upon entrance and the "Post-boarding Process" upon exit, followed by the autonomous cooperative process. This algorithm has succeeded in shortening each processing time [10].



**Fig. 2.** Temporary memory method for fast fare adjustment. When enter at St.A, "F0" and "FAJ" is written in the card. When exit from St.B, "FAJ" + "FAK" - "F0" and "FAB" - "F0" is compared and the less expensive one is selected.

# 4 System Modeling

This chapter introduces the two models of the IC card ticket system to evaluate the performances; One is based on the "decentralized processes on cards and terminals" and the other is on the "centralized processes on a center server." These two models are to compete in the basic functions of the IC card ticket system: checking the "negative" cards and calculating the fares. The negative cards are checked every time the AFCGs process the IC cards. The ways of the fare calculations are different by the models. Either in the decentralized model or in the centralized one, the total processing time T is defined as in Equation 1: the sum of  $T_1$  for "checking and processing the negative cards",  $T_2$  for "calculating the fares," and  $T_3$  for "communicating and the other processions." This formula is the base when the processing times of the two models are discussed.

$$T = T_1 + T_2 + T_0$$
(1)

#### 4.1 Decentralized Model

In the decentralized processes on cards and terminals, the processes are separated in two: the pre-boarding process at the station A and the post-boarding process at the station B. Therefore, the total processing time  $T_{AB}$  can be divided into  $T_A$  for the preboarding process at the station A and  $T_B$  for the post-boarding process (Equation 2).

$$T_{AB} = T_A + T_B \tag{2}$$

 $T_A$  and  $T_B$  are broken down like T in the Equation 1 (Equations 3 and 4).

$$T_{A} = T_{1A} + T_{2A} + T_{0A} \tag{3}$$

$$T_{\rm B} = T_{\rm 1B} + T_{\rm 2B} + T_{\rm 0B} \tag{4}$$

In Equation 3,  $T_{1A}$  is the time for checking and processing the negative cards at the station A while  $T_{2A}$  for calculating the fares and  $T_{0A}$  for communicating and the others there. The same indexing rule applies to Equation 4.

Fig. 3 shows the pre-boarding process at the station A. First, the AFCG checks the "negative" bit in the IC card. Second, it checks whether the ID number of the IC card is on the "negative" list or not. Third, it seeks the fare data. Finally, it calculates the fare.  $T_{0A}$  simply reflects the amount of data to be read and to be written. Hence,  $T_{1A}$ ,  $T_{2A}$ , and  $T_{0A}$  are written as

$$T_{A} = T_{1A} + T_{2A} + T_{0A} = (t_{0} + t_{n}) + (t_{s} + t_{c}) + \{Tr(Dr) + Twr(Dwr)\}.$$
 (5)

 $t_0$ : the time for the negative-bit check (Const.) Tr: the time for reading the IC card

 $t_n$ : the time for the negative-ID check  $t_s$ : the time for seeking the fare data

t<sub>c</sub>: the time for calculating "FAJ"

Dr: the amount of the data to be read (const.) Twr: the time for writing the IC card Dwr: the amount of the data to be written (const.)

In Equation 5,  $t_0$  is the constant time for the negative-bit check.  $t_n$  is the time for the negative-ID check and it is governed by the volume of the "negative" DB, which can be estimated from the number of transactions P.

$$\mathbf{t}_{n} = \boldsymbol{\beta} \mathbf{P} \mathbf{t}_{1} = \boldsymbol{\beta} \mathbf{t}_{1} \int_{\mathbf{c}} \mathbf{p} d\mathbf{t}$$
(6)

 $\beta$ : the rate of "negating" per transaction (const.) P: the total number of transactions

t<sub>1</sub>: the time for checking a negative card (const.)

p: dP/dt (the number of transactions at a period)

As each AFCG has the fare data from the station to the other stations,  $t_s$  is proportional to the number of whole the stations (j-1) while  $t_c$  is targeted on the stations only within the free-ride area (n-1).

$$\mathbf{t}_{s} = \boldsymbol{\gamma}(\mathbf{j} - 1), \qquad \mathbf{t}_{c} = \boldsymbol{\delta}(\mathbf{n} - 1) \tag{7}$$

 $\gamma$ : the time for picking up a fare datum from the fare DB (const.)  $\delta$ : the time for comparing a fare with another (const.)

Then, Equations 5 to 7 deliver Equation 8.

Т

$$A_{A} = t_{0} + \beta t_{1} \int_{\text{Suica start} \to \text{now}} p dt + \gamma j + \delta n + T_{r}(D_{r}) + T_{wr}(D_{wr}) - \gamma - \delta$$
(8)

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Fig. 3. Card-terminal autonomous decentralized processing (at station A).

In the post-boarding process at the station B (Fig. 4), the processing time  $T_B$  has been shown in Equation 4.  $T_{1B}$  and  $T_{0B}$  are considered in the same way as the Equation 5 while extra processions have to be taken into consideration on  $T_{2B}$ :

$$T_{2B} = t_s + t_c + t_{sum2} + t_c + t_2 .$$
(9)

 $t_{sum2}$ : the time for summation of the fares (const.)  $t_2$ : the time for calculating the balance (const.)

Thus,  $T_B$  is expanded as Equation 10.

$$\begin{aligned} \mathbf{\Gamma}_{\mathbf{B}} &= \mathbf{T}_{1\mathbf{B}} + \mathbf{T}_{2\mathbf{B}} + \mathbf{T}_{0\mathbf{B}} \\ &= (\mathbf{t}_{0} + \mathbf{t}_{n}) + (\mathbf{t}_{s} + \mathbf{t}_{c} + \mathbf{t}_{sum2} + \mathbf{t}_{c} + \mathbf{t}_{2}) + \mathbf{T}_{0\mathbf{B}} \\ &= (\mathbf{t}_{0} + \boldsymbol{\beta}\mathbf{P}\mathbf{t}_{1}) + \{\boldsymbol{\gamma}(\mathbf{j}-1) + \boldsymbol{\delta}(\mathbf{n}-1) + \mathbf{t}_{sum2} + \boldsymbol{\delta} + \mathbf{t}_{2}\} + \{\mathbf{T}_{r}(\mathbf{D}_{r}) + \mathbf{T}_{wr}(\mathbf{D}_{wr})\} \\ &= \mathbf{t}_{0} + \boldsymbol{\beta}\mathbf{t}_{1}\int_{\text{Suira start-sum}} \mathbf{p}\mathbf{d}\mathbf{t} + \boldsymbol{\gamma}\mathbf{j} + \boldsymbol{\delta}\mathbf{n} + \mathbf{t}_{sum2} + \mathbf{t}_{2} - \boldsymbol{\gamma} + \mathbf{T}_{r}(\mathbf{D}_{r}) + \mathbf{T}_{wr}(\mathbf{D}_{wr}) \end{aligned}$$
(10)

Equations 2, 8, and 10 lead to the time for the decentralized procession, which is the function of P and j. That is, the total number of transactions and the total number of stations decides the total processing time.  $T_{AB}$  follows up when P and j increase.



Fig. 4. Card-terminal autonomous decentralized processing (at station B).

#### 4.2 Centralized Model

 $T_c$ , the time for the centralized processes on a center server is broken down in the same way as the Equation 1. In case of the centralized procession at the center server, only the "negative" cards are checked at the entrance in the station A. Another check and the fare calculation are done at the station B (Fig. 5). In this case, the processing time is written as Equation 11, with the expansions as in the decentralized processes.

$$\begin{aligned} &\Gamma_{c} = T_{ic} + T_{2c} + T_{0c} \\ &= (t_{0}' + t_{n}') + (t_{s}' + t_{c}' + t_{sum2}' + t_{c}' + t_{2}') + (T_{0} + T_{i}) \\ &= T_{r}(D_{r}) + T_{wr}(D_{wr}) + T_{r}'(D_{r}) + TW(p,nl,cw) + T_{wr}'(D_{wr}) + TR(p,nl',cr) + t_{0}' \\ &+ \beta P t_{i}' + \frac{\gamma' j(j-1)}{2} + 2\delta' (n-1) + t_{sum2}' + \delta' + t_{2}' \end{aligned}$$
(11)  
$$&= \beta t_{i}' \int_{Suicatart \to now} p dt + \frac{\gamma' j^{2}}{2} - \frac{\gamma' j}{2} + 2\delta' n + TW(p,nl,cw) + TR(p,nl',cr) \\ &+ T_{r}(D_{r}) + T_{wr}(D_{wr}) + T_{r}'(D_{r}) + T_{wr}'(D_{wr}) + t_{0}' - \delta' + t_{2}' \end{aligned}$$

 $T_C$ : the processing time at the center server

 $T_{1C}$ : the time for checking and processing the negative cards at the center server

 $T_{2C}$ : the time for calculating the fares at the center server

 $T_{0C}$ : the time for communicating and the others at the center server

TW: the waiting time for the center server

TR: the waiting time for the R/W

nl: the number of the communications lines at the center server (const.)

cw: the average processing rate of a communications line at the center server (const.)

nl': the number of the communications lines at the R/W (const.)

cr: the average processing rate of a communications line at a R/W (const.) TW and TR, the waiting times at the center server and the R/Ws, are delivered from the queuing theory (Equation 12).

$$TW = \frac{\rho(nl\rho)^{nl}}{nl!(1-\rho)^{2}\sum_{i=0}^{nl-1}\left\{\frac{(nl\rho)^{i}}{i!} + \frac{(nl\rho)^{nl}}{nl!(1-\rho)^{2}}\right\}p}, TR = \frac{\rho'(nl'\rho')^{nl'}}{nl'!(1-\rho')^{2}\sum_{i=0}^{nl'-1}\left\{\frac{(nl'\rho')^{i}}{i!} + \frac{(nl'\rho')^{nl'}}{nl'!(1-\rho')^{2}}\right\}p}$$
(12)  
$$\rho = \frac{p}{nl\cdot cw}, \quad \rho' = \frac{p}{nl'\cdot cr}$$

From the Equations 11 and 12, the time for the centralized procession is estimated. It is also the function of P and j.



Fig. 5. Centralized processing (at station B).

# 5 Evaluation

Fig. 6 and 7 are the results of the simulations according to the two models defined in the previous sections. Fig. 6 shows the relationship between the processing time and the number of the stations while the Fig. 7 shows the one between the processing time and the number of the transactions. Both the figures insist that the decentralized process is more effective at middle-sized transportation and bigger though the centralized process wins at fewer stations; 43 stations and 42,700 transactions are the thresholds. The IC card ticket system was started at 424 stations and the number of

the transactions is reaching about 6 million a day. It suggests the decentralized architecture is more suitable for the actual system.



Fig. 6. Comparison: Decentralized and Centralized (by Stations).



Fig. 7. Comparison: Decentralized and Centralized (by Transactions).

### 6 Conclusion

"The development of faster AFC machines" is one of the most important targets to handle the passengers getting on and off in high-density during the rush hours. On the other hand, reliability is indispensable to process the tickets, which are worth notes. Therefore, the ticket system requires both high performance and high reliability. To achieve these requirements, the autonomous decentralized architecture is introduced into the IC card ticket system, where "IC cards," "AFCGs," and a "center server" are constructed as the autonomous units.

This paper emphasizes the "autonomous decentralized algorithm on the fare calculations by IC cards and AFCGs." With this algorithm, the IC cards and the AFCGs calculate the fares separately to realize high performance. As a result of comparison of the two models prepared, the decentralized systems show more effectiveness than the centralized ones as the systems (the numbers of stations and transactions) get bigger. This technology has been introduced into the actual system and is performing very well [11].

Similar methods can be applied to discuss high reliability and assurance quantitatively: Modeling and quantification for high reliability and assurance shall be taken into further consideration.

### References

- Imai, A.: Examination of parameter of size of automatic fare collection gate, OMRON TECHNICS, Vol. 12, No. 1 (1972) 25-40.
- Naka, Y.: Study on complicated passenger flow in a railway station, Railway Technical research report, No. 1079 (1978)
- Shiibashi, A. et al: JR East Contact-less IC Card Automatic Fare Collection System 'Suica.' IEICE TRANSACTIONS on Information and Systems, Vol. E86D, No. 10 (2003) 2699-2707.
- 4. Miki, S.: Total marketing strategy with non-contact IC card, World Congress on Railway Research, Paris, France, vol.1 (1994) 53-58.
- 5. Mori, K.: Needs and technology trends of assurance system, 1st assurance system research, (2000)
- Mori, K.: Autonomous Decentralized System Concept, Data Field Architecture and Future Trends, International Symposium on Autonomous Decentralized Systems, Kawasaki, Japan (1993) 23-34.
- 7. Mori, K. et al.: Autonomous Decentralized System Software Structure and Its Application, IEEE Fall Joint Computer Conference, Dallas, US (1986) 1056-1063.
- Mori, K.: Autonomous Decentralized System [I-VI], J. IEICE, Vol. 84, No. 6, 403-408, No. 7, 484-490, No. 8, 611-617, No. 9, 663-669, No.10, 734-740 (2001)
- 9. Mori, K.: Technology that flaps in the world -Autonomous Decentralized System (1) (2). The Institute of Electrical Engineers of Japan, Vol. 121, No. 2, 100-104, No. 3, 185-190 (2001)
- 10. Miki, S. et al: Contactless smart card AFC trials in East Japan Railway Co., World Congress on Railway Research, Tokyo, Japan (1999) AO3-1.
- 11. Shiibashi, A.: Autonomous decentralized high-speed processing technology in IC card ticket system, and the application, SICE Trans. on Industrial Application, Vol. 4, No. 7 (2005) 41-49.